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PATENTDocket No.: 547-131

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Pei-Pei DING et al.

U.S. Patent Application No. 10/670,190

Filed: September 26, 2003

Confirmation No. 5418

Group Art Unit: 1742

Examiner: William T. LEADER

For: METHOD OF MAKING A MICROSTRUCTURE USING A CIRCUIT BOARD

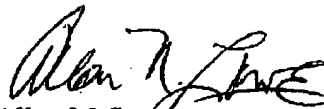
STATUS INQUIRYCommissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The applicant respectfully requests that the undersigned attorney be informed of the status of the referenced application filed on September 26, 2003.

Respectfully submitted,

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